

Amendments to the Claims:

This listing of claims replaces all prior versions, and listings, of claims in the application:

Listing of Claims:

1-22. (Cancelled).

23. (Currently Amended) A communication system comprising a monitor, memory, a bus and ~~one or more~~ a plurality of resources, said memory being connected to the monitor via said bus and arranged for storing tasks and data, each of said resources being connected to the monitor via said bus and arranged for at least one of performing a function and executing a program, wherein said bus is implemented by a plurality of adjacent sections, each section being implemented as an ASIC connected to ~~[[a]] at least one of said plurality of resources~~, said ASIC being arranged to assign sub busses of said bus with variable width.

24. (Previously Presented) Communication system according to claim 23, wherein said resources that are arranged to execute a program are also arranged to generate trigger signals and send them to the monitor, said monitor being arranged to receive said trigger signals, to read one or more tasks related to said trigger signals from said memory, to check whether resources required for performing said task are available and sending commands to selected resources specifying the task to be performed via said bus.

25. (Previously Presented) Communication system according to claim 23, wherein said resources are arranged for mutual communication via said bus.

26. (Previously Presented) Communication system according to claim 23, wherein using the bus is based on a datagram principle.

27. (Previously Presented) Communication system according to claim 23, wherein said memory comprises a task memory and a data memory.

28. (Previously Presented) Communication system according to claim 23, wherein said monitor comprises a state machine sequencer for handling several state machines in parallel.

29. (Previously Presented) Communication system according to claim 28, wherein said memory comprises a ROM portion and a RAM portion, said ROM portion storing state machine definitions for said state machine sequencer, task definitions and default structures, said RAM portion storing dynamic data.

30. (Previously Presented) Communication system according to claim 29, wherein said RAM portion stores a resource allocation table, a data block list, and data blocks.

31. (Previously Presented) Communication system according to claim 23, wherein said monitor comprises an executor arranged for:

- sending commands to resources;
- sending task block requests to memory;
- receiving status information from resources;
- receiving task blocks from memory.

32. (Previously Presented) Communication system according to claim 30, wherein said monitor comprises an executor arranged for:

- sending commands to resources;
- sending task block requests to memory;
- receiving status information from resources;
- receiving task blocks from memory;
- maintaining said resource allocation table.

33. (Currently Amended) Communication system according to claim 23, wherein said resources comprises at least one of:

- a transmitter,
- a receiver,
- an analogue signal manifold,
- a digital to analogue converter,
- an analogue to digital converter,
- a control unit, and
- a digital signal processor.

34. (Previously Presented) Communication system according to claim 33, wherein said resources comprise at least one digital signal processor storing an executable image for performing a program.

35. (Previously Presented) Communication system according to claim 23, wherein said communication system is a radio base unit.

36. (Previously Presented) Communication system according to claim 23, wherein each said ASIC comprises a bus control unit.

37. (Previously Presented) Communication system according to claim 23, wherein communications transmitted via said bus are multiplexed.

38. (Previously Presented) Communication system according to claim 23, wherein each said ASIC comprises a matrix structure with a plurality of cross points arranged to couple input lines with output lines.

39. (Previously Presented) Communication system according to claim 38, wherein said cross points are arranged to allow to isolate a group of input and output lines.

40. (Previously Presented) Communication system according to claim 38, wherein said cross points are arranged to allow to shift connections between input and output lines.

41. (Previously Presented) Communication system according to claim 23, wherein said bus is arranged on different boards that can be connected to one another.

42. (Currently Amended) Method of operating a communication system comprising a monitor, memory, a bus and ~~one or more~~ a plurality of resources, said memory being connected to the monitor via said bus and storing tasks and data, each of said resources being connected to the monitor via said bus, said bus being implemented by a plurality of adjacent sections, each section being implemented as an ASIC connected to ~~[[a]]~~ at least one of said plurality of resources, said method comprising:

assigning sub busses of said bus with variable width; and,

transmitting communications between said monitor, said memory and said ~~one or more~~ plurality of resources via said sub busses.

43-44. (Cancelled)

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